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Research Article

Convergence Study of Minimizing the Nonconvex Total Delay Using the Lane-Based Optimization Method for Signal-Controlled Junctions

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This paper presents a 2D convergence density criterion for minimizing the total junction delay at isolated junctions in the lane-based optimization framework. The lane-based method integrates the design of lane markings and signal settings for traffic movements in a unified framework. The problem of delay minimization is formulated as a Binary Mix Integer Non Linear Program (BMINLP). A cutting plane algorithm can be applied to solve this difficult BMINLP problem by adding hyperplanes sequentially until sufficient numbers of planes are created in the form of solution constraints to replicate the original nonlinear surface in the solution space. A set of constraints is set up to ensure the feasibility and safety of the resultant optimized lane markings and signal settings. The main difficulty to solve this high-dimension nonlinear nonconvex delay minimization problem using cutting plane algorithm is the requirement of substantial computational efforts to reach a good-quality solution while approximating the nonlinear solution space. A new stopping criterion is proposed by monitoring a 2D convergence density to obtain a converged solution. A numerical example is given to demonstrate the effectiveness of the proposed methodology. The cutting-plane algorithm producing an effective signal design will become more computationally attractive with adopting the proposed stopping criterion.

1. Introduction

Over the past decades, various traffic modeling techniques have been developed and studied by many researchers (e.g., [1–8]). In the context of signal settings optimization at isolated junctions, it is always assumed that traffic pattern is arrived dynamically according to Poisson distribution. Pioneering works can be found in [9–11] for the stage-based signal timing design approach and incorporated into a commercial package called OSCADY. Improta

and Cantarella [12] proposed a better phase-based approach to the problem of calculating signal settings. The cycle structure was specified by a set of binary variables relating to incompatible signal groups. A branch-and-bound technique was employed to solve the Binary Mixed Integer Linear Program (BMILP). Gallivan and Heydecker [13] developed a similar approach, in which the cycle structure was specified by one of the stage sequences. Heydecker [14] further reduced this problem difficulty by introducing a procedure to group all possibilities into a much smaller number of equivalence classes. The cycle structure of each equivalence class was represented by a "successor function" that makes the method computationally faster.

Wong and Heydecker [15] and Wong and Wong [16] proposed a lane-based optimization method for determining the capacity-maximizing and cycle-length-minimizing signal settings for an isolated junction. The present study extends the lane-based optimization method for determining a combined set of lane markings and signal settings to minimize the traffic delay at an isolated signal-controlled junction. A cutting plane algorithm with a faster convergence criterion is proposed to solve the delay minimization problem. A numerical example is given to demonstrate the effectiveness of the proposed methodology. On the other hand, the concept of the convergence density in this paper originates from the posterior probability densities of the parameter values of a model in the probabilistic system identification method (e.g., [17–20]). Instead of pinpointing particular values assigned to the parameters of a model, the probabilistic identification method focuses on those parameter values with the highest posterior probability densities. This is similar to that the stopping criterion in this paper focuses on the subdomain formed by a set of gridlines with the highest density and the corresponding parameter values.

2. The Lane-Based Optimization Method

Saturation flow of a traffic lane prescribes the maximum number of vehicles that can leave a junction if full-green signal is given, and this discharge rate is defined by the following expression, which is generalized from that of Kimber et al. [21], that gives the maximum discharge flow rate of traffic flow on a traffic lane of different lane type, width, turning traffic proportion, and physical turning radius:

$$s_{i,k} = \frac{\overline{s}_{i,k}}{1 + 1.5 \sum_{j=1}^{N_T - 1} P_{i,j,k} / r_{i,j,k}},$$
(2.1)

where $s_{i,k}$ is the saturation flow of lane k in arm i, $r_{i,j,k}$ and $P_{i,j,k}$ are the radius of the turning trajectory (= ∞ for straight-ahead movement) and the proportion of flow from arm i to arm j via lane k, and $\overline{s}_{i,k}$ is the saturation flow of the lane if it is for straight-ahead movement only. In the lane-based optimization method, the control variables can be specified as follows. Let $\Lambda = (\Lambda_b, \Lambda_c)$ be the set of control variables, where Λ_b and Λ_c are the subsets of binary and continuous variables, respectively.

The subset Λ_b consists of the following binary variables.

Permitted movements:
$$\boldsymbol{\delta} = (\delta_{i,j,k}, \ j=1,\ldots,N_T-1; \ k=1,\ldots,L_i; \ i=1,\ldots,N_i).$$
 Successor functions: $\boldsymbol{\Omega} = (\Omega_{i,j,l,m}, \ ((i,j),(l,m)) \in \Psi_s).$

The subset Λ_c consists of the following continuous variables.

Designed lane flows: $\mathbf{q} = (q_{i,j,k}, j = 1,..., N_T - 1; k = 1,..., L_i; i = 1,..., N_T)$.

Common flow multiplier: μ .

Cycle length: ζ .

Starts of green for movements: $\theta = (\theta_{i,j}, j = 1,..., N_T - 1; i = 1,..., N_T \text{ and } j = 1; i = 1,..., N_P)$.

Durations of green for movements: $\varphi = (\phi_{i,j}, j = 1,..., N_T - 1; i = 1,..., N_T \text{ and } j = 1; i = 1,..., N_P).$

Starts of green for traffic lanes: $\Theta = (\Theta_{i,k}, k = 1,..., L_i; i = 1,..., N_T)$.

Durations of green for traffic lanes: $\Phi = (\Phi_{i,k}, k = 1,..., L_i; i = 1,..., N_T)$,

where i, m, j are subscripts denoting arms connecting to a junction, k is subscript denoting a traffic lane, N_T is total number of traffic arms in a junction, L_i is number of traffic lane from arm $i,q_{i,j,k}$ is assigned lane flow on traffic lane k for the traffic turning from arm i to arm j, $Q_{i,j}$ is given demand of traffic flow turning from arm i to arm j, μ is the common flow multiplier, $\delta_{i,i,k}$ is the permitted movement on a traffic lane k for the traffic turning from arm i to arm j in the form of a lane marking (= 1 exists/permitted or = 0 otherwise), $\Gamma(i,j)$ is a user-defined mathematical function to find out the exit direction (i.e., which arm the approach traffic is leaving) from the jth destination arm counting from arm i, $E_{\Gamma(i,j)}$ is maximum number of exit lane for leaving traffic toward arm $\Gamma(i,j)$, ζ is the operating cycle length (= reciprocal of cycle time), c_{max} is the maximum allowable limit of cycle time, c_{min} is the minimum allowable limit of cycle time, M is an arbitrary large integral number, $\theta_{i,j}$ is the start of green for a signal phase controlling the traffic turn from arm i to arm j, $\Theta_{i,k}$ is the start of green for a signal phase controlling the traffic turn from arm i on lane k, $\phi_{i,j}$ is the duration of green for a signal phase controlling the traffic turn from arm i to arm j, $\Phi_{i,k}$ is the duration of green for a signal phase controlling the traffic turn from arm i on lane k, $g_{i,j}$ is the minimum duration of green for a signal phase for the traffic turn from arm i to arm j, $\Omega_{i,j,l,m}$ is a successor function governing the sequence of signal display within a signal cycle (= 1 if start of green of signal group (i, j) follows that of signal group (l, m) or = 0 if the opposite is true), Ψ_s is set of incompatible signal groups, Ψ is set of incompatible traffic movements, $\bar{s}_{i,k}$ is saturation flow (maximum discharge flow rate) for straight-ahead movement on a traffic lane k from arm i, $r_{i,j,k}$ is turning radius for traffic turning from arm i to arm j one lane k, e is difference between effective green time and display green time durations (usually 1 second), and $p_{i,k}$ is maximum allowable degree of saturation (demand flow to saturation flow ratio) on a traffic lane *k* from arm *i* (usually take 90%).

To formulate the present total delay optimization problem in the form of a Binary Mixed Integer Linear Program, linear constraint sets below are required to outline the feasible solution region ensuring safe operation of traffic signal plans and logical sequence of signal timings.

Flow Conservation Balancing the Traffic Demand and Optimal Lane Flow Pattern

Consider

$$\mu Q_{i,j} = \sum_{k=1}^{L_i} q_{i,j,k}, \quad \forall j = 1, \dots, N_T - 1; \ i = 1, \dots, N_T.$$
 (2.2)

Equation (2.2) is an equality constraint set mainly to ensure the sum of all optimized lane flows to be assigned on each traffic lane that will eventually be equal to the input turning demand flows.

Minimum Permitted Movement on a Lane

Consider

$$\sum_{j=1}^{N_T-1} \delta_{i,j,k} \ge 1, \quad \forall k = 1, \dots, L_i; \ i = 1, \dots, N_T.$$
 (2.3)

This set of constraint is developed to ensure each traffic lane will be utilized to let through at least one traffic movement.

Maximum Permitted Movements at the Exit

Consider

$$E_{\Gamma(i,j)} \ge \sum_{k=1}^{L_i} \delta_{i,j,k}, \quad \forall j = 1, \dots, N_T - 1; \ i = 1, \dots, N_T.$$
 (2.4)

Equation (2.4) is a constraint set to make sure that there is sufficient number of traffic lanes at downstream exit to manage the vehicles leaving from upstream approach lanes for various turning directions.

Permitted Movements Across Adjacent Lanes

Consider

$$1 - \delta_{i,j,k+1} \ge \delta_{i,m,k}, \quad \forall m = j+1, \dots, N_T - 1; \ j = 1, \dots, N_T - 2; \ k = 1, \dots, L_i - 1; \ i = 1, \dots, N_T.$$
(2.5)

To prevent internal conflicts, for example, a right turn on left-hand lane and a left turn on a right-hand lane in which vehicles will be clashed during green signal phases, constraint set in (2.5) is necessary to restrict the formation of lane marking on adjacent traffic lane from the same approach.

Cycle Length

Consider

$$\frac{1}{c_{\min}} \ge \zeta \ge \frac{1}{c_{\max}}.$$
 (2.6)

Practically, all signal timings will be repeated in cycles in which a signal cycle should be selected within maximum and minimum bounds.

Identical lane signal settings for common turns on lanes:

$$M(1 - \delta_{i,j,k}) \ge \Theta_{i,k} - \theta_{i,j} \ge -M(1 - \delta_{i,j,k}), M(1 - \delta_{i,i,k}) \ge \Phi_{i,k} - \phi_{i,j} \ge -M(1 - \delta_{i,i,k}).$$
(2.7)

In the present formulation, green times will be defined on both lane basis or turning basis. Once a specific traffic turn is assigned on a particular traffic lane, their signal settings should be identical and constraint sets in (2.7) should be included.

Start of Green

Consider

$$1 \ge \theta_{i,j} \ge 0, \quad \forall j = 1, \dots, N_T - 1; \ i = 1, \dots, N_T.$$
 (2.8)

Having said that all signal timings will be repeated in signal cycles and thus the start of green time should always be chosen within a signal cycle, (2.8) is given to maintain this design criteria.

Duration of Green

Consider

$$1 \ge \phi_{i,j} \ge g_{i,j}\zeta, \quad \forall j = 1, \dots, N_T - 1; \ i = 1, \dots, N_T.$$
 (2.9)

Similarly, the assigned green time duration for a turning movement should always be less than the duration of a signal cycle. For safety concern, traffic signals should not be changed too frequently so that the assigned green time should always be greater than a user-defined minimum green time. Equation (2.9) is developed for this purpose.

Order of Signal Displays

Consider

$$\Omega_{i,j,l,m} + \Omega_{l,m,i,j} = 1, \quad \forall ((i,j),(l,m)) \in \Psi_s. \tag{2.10}$$

Since conflicting traffic movements from different approaches cannot receive green times simultaneously, a successor function and constraint set in (2.10) should be defined to order the signal display in a safe sequence.

Clearance Time

Consider

$$\theta_{l,m} + \Omega_{i,j,l,m} + M\left(2 - \delta_{i,j,k} - \delta_{l,m,n}\right) \ge \theta_{i,j} + \phi_{i,j} + \omega_{u,v}\zeta, \quad \forall (u,v) \in \Psi. \tag{2.11}$$

With the introduction of a successor function, the order of signal displays can be designed. For those conflicting movements, a clearance time should be given so that the traffic in the common area of a junction can be cleared before other conflicting traffic movement can enter the junction for discharging.

Prohibited Movement

Consider

$$M\delta_{i,j,k} \ge q_{i,j,k} \ge 0$$
, $\forall k = 1, ..., L_i; j = 1, ..., N_T - 1; i = 1, ..., N_T$. (2.12)

To design optimal lane marking pattern, some vehicle turnings will be banned on different traffic lanes. If the lane marking for a particular vehicle turn is prohibited, the corresponding assigned lane flow must be forced to be zero. Hence, (2.12) is formulated as one of the design constraint sets.

Flow Factor

Consider

$$M(2 - \delta_{i,j,k} - \delta_{i,j,k+1}) \ge \frac{1}{\overline{s}_{i,k}} \sum_{j=1,\dots,N_{T}-1} \left(1 + \frac{1.5}{r_{i,j,k}} \right) q_{i,j,k} - \frac{1}{\overline{s}_{i,k+1}} \sum_{j=1,\dots,N_{T}-1} \left(1 + \frac{1.5}{r_{i,j,k+1}} \right) q_{i,j,k+1}$$

$$\ge -M(2 - \delta_{i,j,k} - \delta_{i,j,k+1}), \quad \forall k = 1,\dots,L_{i} - 1; \ i = 1,\dots,N_{T}.$$
(2.13)

If an identical lane marking is assigned on two adjacent lanes, the flow factors for the two traffic lanes must be identical to ensure the queue lengths are equal. Equation (2.13) should be included to prevent unequal vehicle queue development.

Maximum Acceptable Degree of Saturation

Consider

$$\Phi_{i,k} + e\zeta \ge \frac{1}{p_{i,k}\bar{s}_{i,k}} \sum_{j=1,\dots,N_T-1} \left(1 + \frac{1.5}{r_{i,j,k}}\right) q_{i,j,k}, \quad \forall k = 1,\dots,L_i; \ i = 1,\dots,N_T.$$
 (2.14)

In the present design framework, it is necessary to provide sufficient green time for different traffic movements such that their demand flow to capacity ratios are always within a user acceptable level. Equation (2.14) is developed to ensure the assignment of enough green time to handle the demand traffic.

For isolated signal control junctions, there are three common criteria for optimizing the signal settings: capacity maximization, cycle length minimization, and delay minimization. Alternative signal settings can be generated to fulfill specific operating requirements if different optimizing objectives are chosen. In general, the delay function D that is used as the objective for optimization is nonlinear. The problem has to be formulated as the BMINLP that is shown as follows:

$$\underset{\Lambda = (\Lambda_b, \Lambda_c)}{\text{Minimize } D} \tag{2.15}$$

subject to the linear constraints in (2.2)–(2.14) and μ = 1, where D defines the total delay of the junction.

3. Delay Optimization and 2D Convergence Density

Due to the complicated high dimension solution space and the nonconvexity of the objective delay function, the piecewise linearization approach is not applicable to the present problem. A classical cutting plane algorithm with a 2D convergence density is proposed and discussed in the following sections to solve the BMINLP problem. Now, consider the auxiliary continuous variable λ as the objective function which is the approximated delay found in the cutting plane algorithm (it is approximated as the solution is from the set of linearized hyperplane but not from the original nonlinear delay function), with the following additional nonlinear constraint:

$$D - \lambda \le 0. \tag{3.1}$$

Then, the stopping criterion is considered and the 2D convergence density function is defined by

$$\pi(\Delta, x_o, y_o) = \frac{\operatorname{Count}(x_o \le x_\kappa \le x_o + \Delta \wedge y_o \le y_\kappa \le y_o + \Delta)}{\overline{\kappa}}, \quad \text{for } \kappa = 1 \text{ to } \overline{\kappa}, \tag{3.2}$$

where $x_{\kappa} = \tilde{\lambda}_{\kappa}$ and $y_{\kappa} = \tilde{D}_{\kappa}$; $\overline{\kappa}$ is the number of iterations; Count is a function which counts the times of x_{κ} and y_{κ} that match the specified criteria. x_{o} and y_{o} are the coordinates of any grid point within the domains of λ and D. \tilde{D}_{κ} and $\tilde{\lambda}_{\kappa}$ are the numerical values at the solution point. Note that the domains λ and D are divided by a set of rectangular grid lines; the interval between any two grid lines is Δ . The stopping requirement is that there is one grid box of x_{o} and y_{o} with the highest convergence density value for the last n iterations. If the interval of Δ is smaller (i.e., the grid box size is smaller), it requires more iterations for the coordinates of x_{κ} and y_{κ} staying the same grid box in the last n iterations to achieve the highest convergence

density value. It is noted that the nonlinear constraint is linearized by a first-order Taylor's series expansion to form a linear function L^{κ} , where

$$L^{\kappa} = \left[\left(\sum_{i=1}^{N_{T}} \sum_{k=1}^{L_{i}} D_{i,k}^{\kappa} - \lambda^{\kappa} \right) + \alpha^{\kappa} \left(\sum_{i=1}^{N_{T}} \sum_{k=1}^{L_{i}} \left(\left(\frac{\partial D_{i,k}}{\partial \gamma_{i,k}} \right)^{\kappa} (\gamma_{i,k} - \gamma_{i,k}^{\kappa}) + \left(\frac{\partial D_{i,k}}{\partial \zeta} \right)^{\kappa} (\zeta - \zeta^{\kappa}) \right. \right. \\ \left. + \sum_{j=1}^{N_{T}-1} \left(\frac{\partial D_{i,k}}{\partial q_{i,j,k}} \right)^{\kappa} \left(q_{i,j,k} - q_{i,j,k}^{\kappa} \right) \right)$$

$$\left. - (\lambda - \lambda^{\kappa}) \right) \right] \leq 0,$$

$$(3.3)$$

in which κ specifies the κ th linearization point, $\gamma_{i,k} = \Phi_{i,k} + e\zeta$, and α^{κ} is a parameter that controls the degree of a cut in the feasible region. The lane-based delay minimization problem becomes the minimization of λ , subject to constraints in (2.2)–(3.1), $\mu = 1$, and all of the constraints (3.3) that were generated in previous iterations.

4. Numerical Example

The following steady-state delay formula, based on random arrivals and regular departure patterns, is used [9, 10]:

$$D_{i,k} = \frac{9}{10} \left(\frac{\sum_{j} q_{i,j,k} (1 - \gamma_{i,k})^2}{2\zeta (1 - y_{i,k})} + \frac{(y_{i,k}/\gamma_{i,k})^2}{2(1 - y_{i,k}/\gamma_{i,k})} \right), \tag{4.1}$$

where $D_{i,k}$ is the rate of delay for the traffic lane k on arm i of a signal junction. The total rate of delay of the junction, D, is the sum of the delays for all traffic lanes on all arms $D = \sum_{i=1}^{N_T} \sum_{k=1}^{L_i} D_{i,k}$. This formula is widely used for the estimation of junction delay.

Consider a four-arm junction with four traffic lanes on each arm, as shown in Figure 1. The maximum cycle length is set at 120 seconds, and the maximum acceptable degree of saturation is 90% in all lanes. The minimum greens are 5 seconds for traffic movements. The traffic demands are given in Table 1. The required clearance time for any two conflicting movements (including both traffic and pedestrian movements) is 6 seconds. We also take the solution of capacity maximization as the starting point for linearization in the cutting plane algorithm. Set all initial α 's = 1.0, and a multiplication factor 10.0 is applied to update the α 's each time there is an invalid underestimator. The cutting plane algorithm is then solved by a commercial package called "The mathematical programming language" in which a CPLEX solver is installed to solving the Binary Mixed Integer Linear Program.

Now, consider the stopping requirements in (3.2). In this example (see Figure 2), n=10 and $\Delta=2$ (the domain of λ and D are 0 to 50). The two-dimensional plot of the convergence density is shown in Figure 3. It can be seen that from 84th to 93rd iterations, the convergence density value of the marked grid box is the highest. The minimum delay from the cutting plane algorithm is 26.683 veh-s/s, and the corresponding optimized cycle length to operate the junction is 71.24 seconds.

Traffic demand	in veh/hr	To arm					
		1	2	3	4		
	1	_	500	200	100		
From arm	2	100	_	100	500		
	3	300	300	_	300		
	4	100	400	400			

Table 1: The traffic demand for the example junction.

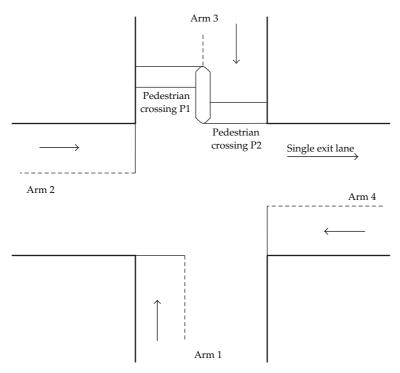


Figure 1: The layout of the four-arm example junction.

Typical results of the proposed solution algorithms, including the lane permitted movements, designed lane flows, and the signal settings, are given in Table 2. The total delays, based on Webster's delay function, are calculated and put in the last column of the tables. It can also be verified that the degrees of saturation for all traffic lanes are within the specified upper limit of 90%. Corresponding optimized lane marking pattern is given in Figure 3 diagrammatically. A summary of the model statistics for the example calculations is presented in Table 3.

Figures 4(a) and 4(b) show the number of all iterations and the minimum delay against the number of iterations in the highest density box, respectively. It can be seen that the smaller the grid size is, the higher the number of all iterations is; the higher the number of iterations required in the highest density box is, the higher the number of all iterations is. Figures 4(c) and 4(d) show the number of all iterations and the minimum delay against the relative error which is the main parameter in the stopping criteria in [22]. Similar to the observation in Figures 4(a) and 4(b), the minimum delay can converge to a certain value when the number of all iterations increases. The main differences between the convergence patterns in the present

 Table 2: Optimization results from the cutting plane algorithm.

	(2.13)	Total	aciay	1.0148	1.0808	1.8419	1.2849	0.3575	0.3812	5.2316	0.7256	1.1842	3.0261	1.7250	1.7250	0.3123	2.9411	1.9255	1.9255	26.6830
	(2.12)	Green	100	0.0	0.0	16.8	35.4	26.8	26.8	46.4	0.0	0.0	16.8	32.8	32.8	35.4	46.4	71.2	71.2	
	(2.11)	Degree of		0.3541	0.3541	0.6129	0.6346	0.2086	0.2086	0.8514	0.3238	0.4029	0.7462	0.6669	0.6669	0.1321	0.6811	0.6475	0.6475	
	(2.10)	Effective	Piccii	27.8	27.8	11.0	0.9	9.4	9.4	19.9	11.8	30.4	13.6	8.6	9.8	30.9	19.9	11.8	11.8	
	(2.9)	у		0.1382	0.1382	0.0950	0.0534	0.0276	0.0276	0.2375	0.0534	0.1718	0.1425	0.0802	0.0802	0.0573	0.1900	0.1069	0.1069	
24 seconds	(2.8)	Saturation	1011	1746.7	1871.1	2105.0	1871.1	1746.7	1871.1	2105.0	1871.1	1746.7	2105.0	1871.1	1871.1	1746.7	2105.0	1871.1	1871.1	
$\frac{1}{2}$ le length = 71.	(8)	Sum of Turning Saturation	Proportion	1.00	1.00	0.00	1.00	1.00	1.00	0.00	1.00	1.00	0.00	1.00	1.00	1.00	0.00	1.00	1.00	
Optimal cvc	(2.7)	Sum of	ianc non	241.4	258.6	200.0	100.0	48.3	51.7	500.0	100.0	300.0	300.0	150.0	150.0	100.0	400.0	200.0	200.0	
			4				100.0			500.0		300.0								
)–(2.6)	To arm	8			200.0		48.3	51.7									200.0	200.0	
	(2.3)	To	2	241.4	258.6									150.0	150.0		400.0			
			\vdash								100.0		300.0			100.0				
	(2.2)	Lane			2	3	4		2	3	4	<u></u>	2	3	4		2	3	4	
	(2.1)	From		1	1	1		2	2	2	2	3	3	3	3	4	4	4	4	

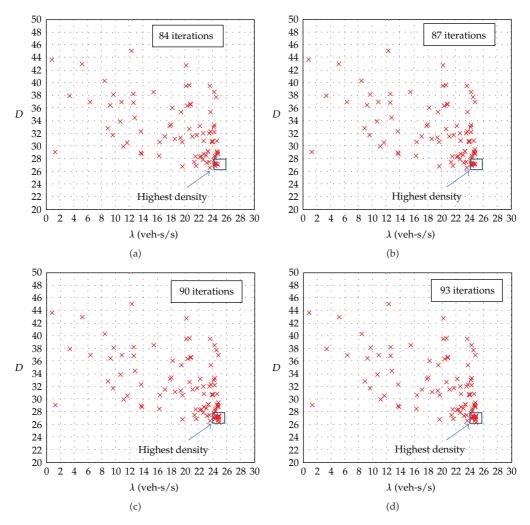


Figure 2: Two-dimensional plot of the convergence density.

study and [22] are that the curves representing the numbers of all iterations in Figure 4(a) are more linear; the minimum delay in Figure 4(b) can converge faster to a certain value against the number of iterations required in the highest density box (the curve looks more concave).

5. Conclusions

Lane-based optimization models have been presented in this paper. While the capacity maximization and cycle length minimization for isolated signal-control junctions are formulated as BMILP problems that can be solved by the standard branch-and-bound technique [16], the delay minimization problem for isolated junctions is a BMINLP because of the nonlinear delay function [22]. The cutting plane algorithms with a 2D convergence density stopping criterion have been proposed to solve the problem. The nonlinear delay function is approximated by successive linearizations. The BMINLP problem is transformed

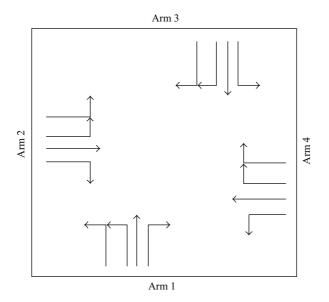


Figure 3: Optimized lane markings for the example problem.

Table 3:	Statistics	of the	example	problem.

Model variables	Number of variables	Constraints	Number of constraints	
Binary variables	142	Cutting planes	93	
Permitted movements	40	Starts of green	58	
Successor functions	102	Durations of green	60	
		Order of signal displays	51	
Continuous variables	110	Clearance times	68	
Lane flows	48	Cycle length	1	
Starts of green	29	Maximum acceptable degree of saturation	16	
Durations of green	30	Flow conservations	12	
Cycle length	1	Minimum permitted movements in a lane	8	
Common flow multiplier	1	Maximum permitted movements at exits	3	
Auxiliary variable	1	Prohibited movements	40	
		Permitted movements across adjacent lanes	72	
		Flow factors	72	
		Lane signal settings	176	
		Others	53	
Total number	252		783	

into a series of BMILP problems that are solved by the standard branch-and-bound technique. To achieve a more cost-effective method for the lane-based optimization of delay at isolated signalized junctions, the cutting plane algorithm should be applied together with the new proposed stopping criterion taking the 2D convergence density into considerations.

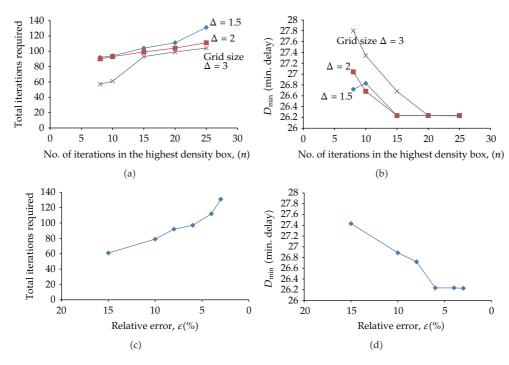


Figure 4: (a) The number of all iterations against the number of iterations in the highest density box. (b) The minimum delay against the number of iterations in the highest density box. (c) The number of all iterations plotted against the relative error (the stop criteria in [22]). (d) The minimum delay against the relative error.

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